

# 2SA1022

## Silicon PNP epitaxial planar type

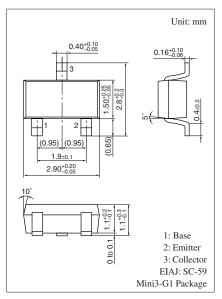
For high-frequency amplification Complementary to 2SC2295

### ■ Features

- High frequency voltage f<sub>T</sub>
- Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing and the magazine packing

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	-30	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-20	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	-5	V	
Collector current	$I_C$	-30	mA	
Collector power dissipation	P <sub>C</sub>	200	mW	
Junction temperature	$T_{j}$	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	



Marking Symbol: E

## ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

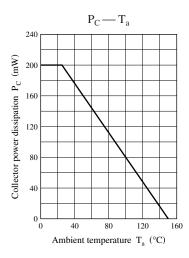
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Base-emitter voltage	$V_{BE}$	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$		- 0.7		V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = -10 \text{ V}, I_E = 0$			- 0.1	μΑ
Collector-emitter cutoff current (Base open)	$I_{CEO}$	$V_{CE} = -20 \text{ V}, I_B = 0$			-100	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = -5 \text{ V}, I_C = 0$			-10	μΑ
Forward current transfer ratio *	h <sub>FE</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$	70		220	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -10 \text{ mA}, I_B = -1 \text{ mA}$		- 0.1		V
Transition frequency	$f_T$	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$	150	300		MHz
Noise figure	NF	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 5 \text{ MHz}$		2.8		dB
Reverse transfer impedance	$Z_{rb}$	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 2 \text{ MHz}$		22		Ω
Reverse transfer capacitance	C <sub>re</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}, f = 10.7 \text{ MHz}$		1.2		pF
(Common emitter)						

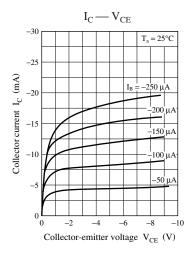
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

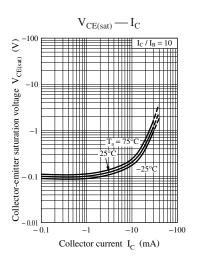
#### 2. \*: Rank classification

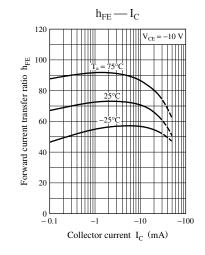
Rank	В	С
$h_{FE}$	70 to 140	110 to 220

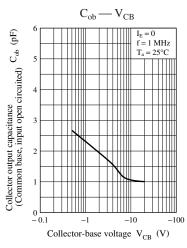
# **Panasonic**

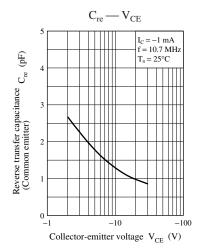


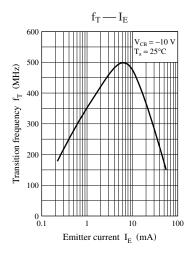


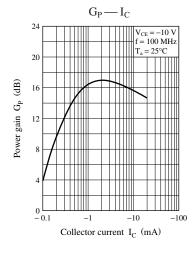


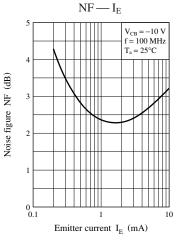












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